

L Number	Hits	Search Text	DB	Time stamp
17	1	("5502621").PN.	USPAT	2004/04/19 13:09
18	1626	(chip or die) with ((conventional or standard) and (mirror or inverted))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 13:13
19	1	("5691570").PN.	USPAT	2004/04/19 13:11
20	3216	(chip or die) with ((normal or conventional or standard) and (mirror or inverted or reverse))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 13:26
21	1800	((chip or die) with ((normal or conventional or standard) and (mirror or inverted or reverse))) and (semiconductor or (integrated adj circuit))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 13:26
22	1229	((((chip or die) with ((normal or conventional or standard) and (mirror or inverted or reverse))) and (semiconductor or (integrated adj circuit))) and (@ad<19990512)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 13:28
23	969	(((((chip or die) with ((normal or conventional or standard) and (mirror or inverted or reverse))) and (semiconductor or (integrated adj circuit))) and (@ad<19990512)) and (terminal or terminal or pad or pads or land or lands)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 13:28
24	1029	(((((chip or die) with ((normal or conventional or standard) and (mirror or inverted or reverse))) and (semiconductor or (integrated adj circuit))) and (@ad<19990512)) and (terminal or terminal or pad or pads or land or lands or pin or pins)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 13:58
25	781	(((((chip or die) with ((normal or conventional or standard) and (mirror or inverted or reverse))) and (semiconductor or (integrated adj circuit))) and (@ad<19990512)) and (terminal or terminal or pad or pads or land or lands or pin or pins)) and (move or turn or position)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 14:51
26	37	6028365.URPN.	USPAT	2004/04/19 14:11
27	13	("5133495" "5229916" "5281852" "5394303" "5397916" "5582326" "5679977" "5682061" "5714405" "5776797" "5801446" "5885549" "5895970").PN.	USPAT	2004/04/19 14:20
29	1	("5977624").PN.	USPAT	2004/04/19 14:53
28	248	(((((chip or die) with ((normal or conventional or standard) and (mirror or inverted or reverse))) and (semiconductor or (integrated adj circuit))) and (@ad<19990512)) and (terminal or terminal or pad or pads or land or lands or pin or pins)) not (((((chip or die) with ((normal or conventional or standard) and (mirror or inverted or reverse))) and (semiconductor or (integrated adj circuit))) and (@ad<19990512)) and (terminal or terminal or pad or pads or land or lands or pin or pins)) and (move or turn or position))	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 14:59